

D:/gitt/MicrochipFor32
/bsp_Device/bsp_SD.h

```
graph TD; A["D:/gitt/MicrochipFor32 /bsp_Device/bsp_SD.h"] --> B["spi.h"]; A --> C["varint.h"]; C --> D["stdint.h"];
```

spi.h

varint.h

stdint.h